Application No: 09/418,647

## **In the Specification:**

Please replace the paragraph at page 5, lines 5-11, of the specification with the following replacement paragraph:

Figure 2 illustrates a block diagram of a network management processor in accordance with the present invention. The network manager 10 is preferably coupled to, or includes, a network management processor 50. The network management processor 50 includes a processing module 52 and a memory 54. The processing module 52 may include a single processing entity or plurality of processing entities. Such a processing entity may be a microprocessor, microcontroller, digital signal processor, state machine, logic circuitry, or any device that processes information based on operational or programming instructions.